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FREESCALE SEMICONDUCTOR, INC.
LAW DEPARTMENT
7700 WEST PARKER LANE MD:TX32/PL02
AUSTIN, TX 78729

02/07/2006 TBESHAH2 00000017 503079 10807624

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Stacie Herrera	(Depositor's name)
<i>Stacie Herrera</i>	(Signature)
February 6, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,624	03/24/2004	Helmut Broch	SCI3191ZP	4056

TITLE OF INVENTION: TRANSISTOR WITH REDUCED GATE-TO-SOURCE CAPACITANCE AND METHOD THEREFOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	02/17/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
QUACH, TUAN N	2826	438-197000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Daniel D. Hill

2 James L. Clingan, Jr.

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Freescale Semiconductor, Inc.

Austin, TX

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐
- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- ☐
- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Daniel D. Hill

Date

2-6-06

Typed or printed name

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35,895

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DATE: February 6, 2006

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(SENDER) (EXTENSION)TOTAL NUMBER OF PAGES 3 (including this page)IF YOU HAVE ANY TROUBLE OR QUESTIONS WITH TRANSMISSION, OR HAVE RECEIVED IT IN
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Docket No.: SC13191ZP

Applicant: Helmut Brech

Serial No.: 10/807,624

Art Unit: 2826

Filed: April 24, 2004

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	<input checked="" type="checkbox"/>	1 page Facsimile Cover Sheet
2.	<input checked="" type="checkbox"/>	1 page PTOL-85B Issue Fee Transmittal (in duplicate)

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